

Docket No.: 43876-151

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
	:	
Craig HANSEN, et al.	:	Confirmation Number: To be assigned
	:	
Serial No.: Continuation of Application No. 10/436,340	:	Group Art Unit: To be assigned
	:	
Filed: November 20, 2003	:	Examiner: To be assigned
	:	
For:		<u>PROGRAMMABLE PROCESSOR AND METHOD FOR MATCHED ALIGNED AND UNALIGNED STORAGE INSTRUCTIONS</u>

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop NEW APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Serial No.: Continuation of
Application No. 10/436,340

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: November 20, 2003

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The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 10/436,340 , filed May 13, 2003 , which is relied upon for an

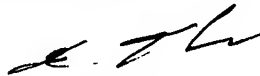
Serial No.: Continuation of
Application No. 10/436,340

earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR
1.98(d).

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U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US 4,876,660	10/24/89	Owen et al.			
		US 4,956,801	09/11/90	Priem et al.			
		US 4,969,118	11/06/90	Montoye et al.			
		US 5,032,865	07/16/91	Schlunt			
		US 5,408,581	04/18/95	Suzuki et al.			
		US 5,500,811	03/19/96	Corry			
		US 5,557,724	9/17/1996	Sampat et al.			
		US 5,588,152	12/24/1996	Dapp et al.			
		US 5,640,543	6/17/1997	Farrell et al.			
		US 5,757,432	5/26/1998	Dulong et al.			
		US 5,802,336	9/1/1998	Peleg et al.			
		US 5,809,292	9/15/1998	Wilkinson et al.			
		US 5,818,739	10/6/1998	Peleg et al.			
		US 5,825,677	10/20/1998	Agarwal et al.			
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)	Publication Date MY Y	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		EP 0474246 A2	9/6/1991				
		EP 0654733 A1	7/5/1994				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		L. Kohn et al. "The Visual Instruction Set (VIS) in UltraSPARC" IEEE. 1995. 462-469.					
		D. Shaver. "A General-Purpose Array Processor for Seismic Processing" (Nov - Dece 1984) January - March 1998. 15th Anniversary Issue. 5-26.					
		R. Lee. "Accelerating Multimedia with Enhanced Microprocessors" IEEE Micro. April 1995. 22-32.					
		N. Margulis. "i860 Microprocessor Architecture" 1990. 8-10, 171-175, 182-183.					
		A. Levinthal et al. "Parallel Computers for Graphics Applications" 1987. 193-198.					
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.

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		US 5,835,782	11/10/1998	Lin et al.			
		US 5,886,732	3/23/1999	Humpleman			
		US 5,922,066	7/13/1999	Cho et al.			
		US 5,983,257	11/9/1999	Dulong et al.			
		US 6,016,538	1/18/2000	Gutttag et al.			
		US 6,092,094	7/18/2000	Ireton			
		US 6,401,194 B1	6/4/2002	Nguyen et al.			
		US 4,025,772	5/24/1977	Constant			
		US 4,489,393	12/18/1984	Kawahara, et al.			
		US 4,701,875	10/20/1987	Konishi et al.			
		US 4,727,505	2/23/1988	Konishi et al.			
		US 4,893,267	1/9/1990	Alsup et al.			
		US 4,975,868	12/4/1990	Freerksen			
		US 5,157,388	10/20/1992	Kohn			
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		K. Diefendorff et al. "Organization of the Motorola 88110 Superscalar RISC Microprocessor" IEEE Micro. April 1992. 40-63.					
		L. Gwennap. "IBM Regains Performance Lead with Power2" Microprocessor Report. October 4, 1993. Vol. 7. No. 13. 1,6-10.					
		L. Gwennap. "IBM Creates Power PC Processors for AS/400" Microprocessor Report. July 31, 1995. 15-16.					
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		US 5,201,056	4/6/1993	Daniel et al.			
		US 5,268,855	12/7/1993	Mason et al.			
		US 5,268,995	12/7/1993	Diefendorff et al.			
		US 5,423,051	6/6/1995	Fuller et al.			
		US 5,426,600	6/20/1995	Nakagawa et al.			
		US 5,592,405	1/7/1997	Gove et al.			
		US 5,642,306	6/24/1997	Mennemeier et al.			
		US 5,666,298	9/9/1997	Peleg et al.			
		US 5,669,010	9/16/1997	Duluk, Jr.			
		US 5,673,407	9/30/1997	Poland et al.			
		US 5,675,526	10/7/0997	Peleg et al.			
		US 5,680,338	10/21/1997	Agarwal et al.			
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(Suppl.) INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 43876-151		SERIAL NO. C ntinuati n f Serial N . 10/436,340	
(PTO-1449)				APPLICANT Craig HANSEN, et al.			
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		US 4,814,976	3/21/1989	Craig C. Hansen, et al			
		US 5,996,057	11/30/1999	Hunter L. Scales, III, et al			
		US 6,041,404	3/21/2000	Patrice Roussel, et al			
		US 6,052,769	4/18/2000	Thomas R. Huff, et al			
		US 6,173,393 B1	1/9/2001	Salvador Palanca, et al			
		US 6,275,834 B1	8/14/2001	Derrick Chu Lin, et al			
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